teletest 16 In-Circuit Emulators

- Full Low-Voltage Support
- Real-Time Emulation Up to 20 MHz Processor Clock
- No Wait States or Target-System Restrictions
- Emulation Memory Up to 1 MB
- Two Independent Trace Buffers
- 8 Hardware Break/Trigger Registers
- Real-Time, Delay and Event Counters
- SAA-Oriented User Interface
- Enhanced HLL-Debugging Facilities
- Serial and Parallel Link to the Host
- Performance Analyses (Optional)

The teletest 16 in-circuit-emulator is a modular system for testing 80x86 applications. The system offers powerful support not only for the 80C86/88 and 80C286 microcontrollers, but also for all 80186/188 microprocessors up to a maximum frequency of 20 MHz (processor clock). The teletest 16 operates without using any target-system resources or wait states, thus offering transparent real-time emulation. The system consists of a basic unit with power supply (110/235 V), emulation memory, two trace buffers, the trigger logic, as well as an emulation pod for the processor-specific features. The EP186 emulation pods support all variants of the 80186 processor family simply by inserting the appropriate processor into the corresponding socket located on the pod.

The teletest 16 system uses the SAA-standard HiTOP operating software, the interactive software for tried and tested all Hitex in-circuit-emulation systems. It allows full symbolic high-level language debugging for all development environments that generate Intel-OMF as well as Microsoft and Borland tools and others. HiTOP provides an extremely user-friendly interface that is tailored to the requirements of 80x86 development work. Over 2,500 teletest 16 emulators have been installed and testify to the powerful features, accuracy, and reliability of the teletest 16 in-circuit-emulation system for complex embedded applications.

PROCESSORS SUPPORTED:
80186 XL/EA/EB/EC, and 80188 XL/EA/EB/EC Processors

AVAILABILITY:
Now

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